L Number	Hits	Search Text	DB	Time stamp
4	14	(macro\$6 or cell) same library same gdsii	USPAT;	2004/08/02 16:23
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
[-	48754	("integrated circuit" or IC) same design\$3	USPAT	2004/07/22 16:32
-	37081	(acertain\$3 or determin\$5) same function same specific\$5	USPAT	2004/07/22 16:32
-	5904	map\$5 same function same specific\$5	USPAT	2004/07/22 16:32
-	24354	(acertain\$3 or determin\$5) same function same (placement	USPAT	2004/07/22 16:32
	80	or location) (("integrated circuit" or IC) same design\$3) and ((acertain\$3)	USPAT;	2004/07/22 16:22
-	80	or determin\$5) same function same specific\$5) and (map\$5	US-PGPUB;	2004/07/22 16:33
		same function same specific\$5) and ((acertain\$3 or	EPO; JPO;	1
		determin\$5) same function same (placement or location))	DERWENT;	
		determines y sume function sume (placement or location)	IBM_TDB	
l _	6317675	block based design	USPAT;	2004/07/22 16:33
	001,0,0	Diodit Bussel assign	US-PGPUB;	2001,07,22 20100
			EPO; JPO;	
	·		DERWENT;	
			IBM_TDB	
-	163	block same design same placement same interconnection	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	((block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
		same function)and GDSII	US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
_	1	(block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
	_	and GDSII	US-PGPUB;	2001/07/22 10:55
		and Spoil	EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	1619	block same design same placement	USPAT;	2004/07/22 16:33
			US-PGPUB;	
1			EPO; JPO;	
			DERWENT;	
	200=26	1	IBM_TDB	2004/07/20 : 5 55
-	208526	layers same vias	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO; DERWENT;	[
			IBM_TDB	
_	80014	clock same power	USPAT;	2004/07/22 16:33
	00011	aloux sumo porior	US-PGPUB;	200 1/07/22 10.00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	421	layers same clock same power	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
[IBM_TDB	
-	1432	clock same power same layer	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

-	13080	rout\$3 same different same (level or layer)	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	İ		IBM_TDB	
-	1167	716/\$.ccls. and (block same size)	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	1
-	1529	716/\$.ccls. and (block same gate)	USPAT;	2004/07/22 16:33
		, ze, questo ana (enera camo gare)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	782	716/¢ cele and (black came transister)	USPAT;	2004/07/22 16:22
-	/02	716/\$.ccls. and (block same transistor)		2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
	1		DERWENT;	
	1		IBM_TDB	
-	22	((("integrated circuit" or IC) same design\$3) and	USPAT;	2004/07/22 16:33
	1	((acertain\$3 or determin\$5) same function same specific\$5)	US-PGPUB;	
		and (map\$5 same function same specific\$5) and	EPO; JPO;	
		((acertain\$3 or determin\$5) same function same (placement	DERWENT;	
		or location))) and 716/\$.ccls.	IBM_TDB	
-	51	"block based design"	USPAT;	2004/07/22 16:33
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	"block based design" and (size same shape)	USPAT;	2004/07/22 16:33
ı		block based design and (Size same shape)	US-PGPUB;	200 1, 07, 22 10.00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	•
_	2352	(716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:33
-	2332	(block same gate)) or (716/\$.ccls. and (block same		2004/07/22 10.33
			US-PGPUB;	
ì		transistor))	EPO; JPO;	
İ			DERWENT;	
İ	1040	(77.67)	IBM_TDB	0004/07/00 46 00
-	1040	((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:33
		(block same gate)) or (716/\$.ccls. and (block same	US-PGPUB;	
		transistor))) and hierarch\$6	EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	27	(block same design same placement same interconnection)	USPAT;	2004/07/22 16:33
	1	same function	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	971	(((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
	_	(block same gate)) or (716/\$.ccls. and (block same	US-PGPUB;	
	1	transistor))) and hierarch\$6) and function	EPO; JPO;	,
		///	DERWENT;	·
			IBM_TDB	
l <u>-</u>	1840	(716/\$.ccls. and (block same gate)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
	10-10	(block same transistor))	US-PGPUB;	230 1/07/22 10:37
		(Plock Suffic dulisistor))	EPO; JPO;	
			DERWENT;	
	<u> </u>		IBM_TDB	l <u></u>

	267	GDSII	USPAT;	2004/07/22 16:34
	207	GD3H	US-PGPUB;	200 1/07/22 10.54
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	348	thousand adj gate	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
	79	(block same design same placement) and (layers same vias)	USPAT;	2004/07/22 16:34
	/	(block suffic design suffic placement) and (layers suffic vias)	US-PGPUB;	2001/07/22 10:51
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	10	(block same design same placement) and trailer	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;]
•	400	amall wast 6 mation #2 mast blad.	IBM_TDB	2004/07/22 46:24
-	496	small near5 function\$2 near5 block	USPAT;	2004/07/22 16:34
•			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	597	small near5 function\$2 near5 (block or core)	USPAT;	2004/07/22 16:34
}		(2000.000)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	7901	716/\$.ccls. (small near5 function\$2 near5 block)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
_	75	716/\$.ccls. and (small near5 function\$2 near5 (block or	USPAT;	2004/07/22 16:34
	"	core))	US-PGPUB;	200 1/ 0//22 2010 1
		33.377	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	65	(block same design same placement) and (glue near3 logic)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
_	16	(block same design same placement) and (layers same clock	USPAT;	2004/07/22 16:35
		same power)	US-PGPUB;	-30 1, 07, 22 10.00
		£/	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	36	(clock same power same layer) and (block same design	USPAT;	2004/07/22 16:34
		same placement)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	1472	glue adj logic	IBM_TDB USPAT;	2004/07/22 16:34
	17/2	gide day logic	US-PGPUB;	200 1/07/22 10:54
			EPO; JPO;]
			DERWENT;	
	L		IBM_TDB	_

			, , 	
-	2094	(power same layer) and (clock same layer)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	2517	(power same layer) and (clock same layer)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1432	(power same layer) same (clock same layer)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	156	(block same design same placement) and (rout\$3 same	USPAT;	2004/07/22 16:34
		different same (level or layer))	US-PGPUB;	2001,07,22 2010 1
			EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
1	89	((power same layer) and (clock same layer)) and 716/\$.ccls.	USPAT;	2004/07/22 16:34
-	09	((power same layer) and (clock same layer)) and 710/\$.ccis.		2004/07/22 10:34
	l		US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3	09/098,599	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1	,	IBM_TDB	
-	1	716/\$.ccls. and ("data path" same memory same control	USPAT;	2004/07/22 16:34
		same i/o same analog)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	716/\$.ccls. and ("data path" same memory same control	USPAT;	2004/07/22 16:34
		same i/o same analog same group)	US-PGPUB;	, ,
		, , , , , , , , , , , , , , , , , , , ,	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	87	"1000 gates"	USPAT;	2004/07/22 16:34
		2000 guitas	US-PGPUB;	200 1,07,22 20.0 1
			EPO; JPO;	
1			DERWENT;	
1			IBM_TDB	
1 -	62	(((((716/\$.ccls. and (block same size)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
	02	((((/10/\$.ccis. and (block same size)) or (/10/\$.ccis. and (block same gate)) or (/16/\$.ccis. and (block same	US-PGPUB;	200 1/07/22 10.37
		transistor))) and hierarch\$6) and function) and(trailer or	EPO; JPO;	
		"glue logic")		
		gide logic)	DERWENT;	
	(0)	//716/t cele and (block come ante)\ c:: /746/t ante and	IBM_TDB	2004/07/22 46:24
-	68	((716/\$.ccls. and (block same gate)) or (716/\$.ccls. and	USPAT;	2004/07/22 16:34
	İ	(block same transistor))) and (trailer or "glue logic")	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	!		IBM_TDB	
-	113	716/\$.ccls. and sub-block	USPAT;	2004/07/22 16:34
			US-PGPUB;	
]		EPO; JPO;	
			DERWENT;	
_			IBM_TDB	

-	65	(thousand adj gate) same block	USPAT; US-PGPUB;	2004/07/22 16:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	39	716/\$.ccls. and ((thousand adj gate) same block)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
-	8	716/8.ccls. and (thousand adj gate)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		71.C/A sale and /Massesand add sales	IBM_TDB	2004/07/22 46-24
-	77	716/\$.ccls. and (thousand adj gate)	USPAT;	2004/07/22 16:34
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
_	71	716/\$.ccls. and (small near5 function\$2 near5 block)	USPAT;	2004/07/22 16:34
-	/1	, , to, g.ccis. and (smail near) function \$2 near)	US-PGPUB;	2007/0//22 10.34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	87	(glue adj logic) and 716/\$.ccls.	USPAT;	2004/07/22 16:34
]	(glad day logicy and 710/\$1000)	US-PGPUB;	200 1/ 0//22 10.5 1
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	"5859999".PN.	USPAT	2004/07/22 16:34
-	76	((power same layer) same (clock same layer)) and	USPAT;	2004/07/22 16:35
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	1	"6014507".PN.	USPAT	2004/07/22 16:35
-	1	"5984510".PN.	USPAT	2004/07/22 16:35
-	1	"5860017".PN.	USPAT	2004/07/22 16:35
-	1 22002	"5841664".PN.	USPAT	2004/07/22 16:35
-	22883	(block or group or macro\$5) same library	USPAT;	2004/07/22 16:37
			US-PGPUB; EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
_	1261	((block or group or macro\$5) same library) and 716/\$.ccls.	USPAT;	2004/07/22 16:37
		(12.2.2.2.2.3.2.2.2.2.2.2.2.2.2.2.2.2.2.2	US-PGPUB;	
	1		EPO; JPO;	
	ļ		DERWENT;	
1			IBM_TDB	
-	660	(block or group or macro\$5) same library same layers	USPAT;	2004/07/22 16:37
			US-PGPUB;	
	1		EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	100	((block or group or macro\$5) same library same layers) and	USPAT;	2004/07/29 14:33
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	L	<u> </u>	IBM_TDB	

-	81	((block or macro\$5) same library same layers) and 716/\$.ccls.	USPAT; US-PGPUB;	2004/07/29 14:34
		7.25, \$155.65	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	30	((block or macro\$5) same library same layers) and	USPAT;	2004/07/29 14:35
		716/\$.ccls.	US-PGPUB;	
		720/\$10001	EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	33	((block or macro\$5) same librar\$4 same layers) and	USPAT;	2004/07/29 14:35
		716/\$.ccls.	US-PGPUB;	2001/07/23 11:05
		7 20, 4100.00	EPO; JPO;	
			DERWENT;	
ŀ			IBM_TDB	
-	14	(macro\$5 same librar\$4 same layers) and 716/\$.ccls.	USPAT;	2004/07/29 14:41
		(US-PGPUB;	
			EPO; JPO;	
	}		DERWENT;	
			IBM_TDB	
_	72	(macro\$5 same (wir\$5 or intercon\$8) same layers) and	USPAT;	2004/07/29 15:49
		716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	macro\$5 same netlist same layout same pin same speed	USPAT;	2004/07/29 15:52
			US-PGPUB;	' '
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	59	macro\$5 same netlist same layout	USPAT;	2004/07/29 15:54
1]	•	US-PGPUB;	
			EPO; JPO;	:
			DERWENT;	
			IBM_TDB	
-	9	macro\$5 same netlist same layout same hard	USPAT;	2004/07/29 16:38
			US-PGPUB;	i
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1 1	"5883814".PN.	USPAT	2004/07/29 15:57
-	25	cell same netlist same layout same hard	USPAT;	2004/07/29 16:39
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	4.0	coll come noblish come levelsh come had a come from the	IBM_TDB	2004/00/02 44 47
-	13	cell same netlist same layout same hard same (connec\$5 or	USPAT;	2004/08/02 14:47
		pin)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
I _	17	"5880971"	IBM_TDB	2004/09/02 14:52
-	1/	J00U3/1	USPAT; US-PGPUB;	2004/08/02 14:52
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	0	"seemingly insurmountable job of designing a highly	USPAT;	2004/08/02 14:53
	"	complex circuit is broken into small, workable design	US-PGPUB;	2007/00/02 17.33
		projects"	EPO; JPO;	
		p. 0,000	DERWENT;	
			IBM_TDB	
	L		I TOLL LOD	

	, - · · -			
-	3	"seemingly insurmountable job"	USPAT;	2004/08/02 16:00
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	109	(macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:02
	1	same shape	US-PGPUB;	
	1		EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
-	0	(((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:04
1		same shape) and 716/\$.ccls.) and gdsiii	US-PGPUB;	
ļ			EPO; JPO;	
1			DERWENT;	
1			IBM_TDB	
-	1	GDSIII	USPAT;	2004/08/02 16:03
	ļ	,	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	270	GDSII	USPAT;	2004/08/02 16:03
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:23
1		same shape) and 716/\$.ccls.) and gdsii	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	65	((macro\$6 or cell) same library same different same size	USPAT;	2004/08/02 16:05
[same shape) and 716/\$.ccls.	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		<u> </u>	IBM_TDB	